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Docket No.: M4065.0900/P900
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ramin Ghodsi

Application No.: 10/822,785

Filed: April 13, 2004

Art Unit: N/A

For: MULTI-CELL RESISTIVE MEMORY
ARRAY ARCHITECTURE WITH SELECT
TRANSISTOR

Examiner: Not Yet Assigned

SUBMISSION OF FORMAL DRAWINGS

MS PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (thirteen sheets, thirteen figures) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the informal drawings submitted with the originally filed application.

Dated: May 25, 2004

Respectfully submitted,

By 

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